

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Thakur et al.

Serial No.: 09/921,423

Filed: August 2, 2001

For: A METHOD TO FORM
CORRUGATED STRUCTURE FOR
ENHANCED CAPACITANCE

Confirmation No.: 8029

Examiner: J. Mondt

Group Art Unit: 2826

Attorney Docket No.: 2269-3978.1US
(95-0064.01/US)

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EL994823396US

Date of Deposit with USPS: April 4, 2005

Person making Deposit: Steve Wong

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

The Examiner indicates:

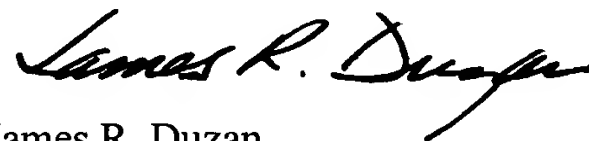
[a semiconductor memory device with capacitor wherein at least one layer of germanium borophosphosilicate glass has a portion that is in contact with a layer of borophosphosilicate glass has not been found in the prior art. Although semiconductor memory devices with capacitor cell including either borophosphosilicate glass or germanium boro-phosphosilicate glass as materials along roughened interfaces are known (see, e.g., Ando (6,097,053) or Sandhu et al (6,249,019 B1), Sandhu et al. (6,124,607) as made of record by Applicant through the IDS filed 10/21/2004), within the context of independent claims 1 through 9 a semiconductor memory device wherein a portion of a layer of germanium boro-

phosphosilicate glass contacts at least a portion of a layer of boro-phosphosilicate glass has not been found. In this regard it is noted in particular in light of Applicant's filing of aforementioned IDS that while in Sandhu et al. (6,249,019 B1) layer 312 comprises germanium borophosphosilicate (col. 6, l. 45-61) the layer in contact therewith is (amorphous, poly-) silicon (col. 7, 21-30), not borophosphosilicate glass (BPSG).

Finally, Sandhu (US2003/0038309A1; USPAT 6,204,196, USPAT 6,040,020) herewith made of record by examiner teach the exploitation of the material differences between BPSG and Ge-BPSG for surface planarization layers (see e.g. section [0020] in US 2003/0038309), not for capacitor sidewall layers. An update search has not revealed pertinent art.]

Applicants concur with the reasons as stated by the Examiner insofar as they comprise a summary, and are exemplary and not limiting. However, the independent claims as allowed include other and different language than that specified by the Examiner, and the allowed dependent claims include other and further features and elements. Accordingly, the scope of the claims must be determined from the literal language of each as a whole, as well as equivalents thereof.

Respectfully submitted,



James R. Duzan
Registration No. 28,393
Attorney for Applicants
TRASKBRITT
P.O. Box 2550
Salt Lake City, Utah 84110-2550
Telephone: 801-532-1922

Date: April 4, 2005
JRD/dlm:lmh

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January 4, 2005

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TRANSMITTAL LETTER

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith Part B - Fee(s) Transmittal for the above-captioned application and a check in the amount of \$1,715.00 in payment therefor plus five (5) copies of the patent when issued.

Also enclosed are Supplemental Application Data Sheet; Amendment Pursuant to 37 C.F.R. § 1.312(a); and Fee Addressee for Receipt of PTO Notices Relating to Maintenance Fees.

Applicants understand that no additional fees are required. However, if the Office determines that any comparison fees or other additional fees are required, the Commissioner is authorized to charge any such fees to TraskBritt Deposit Account No. 20-1469. A copy of this Transmittal Letter is enclosed for deposit account charging purposes.

Respectfully submitted,



James R. Duzan
Registration No. 28,393
Attorney for Applicants
TRASKBRITT
P.O. Box 2550
Salt Lake City, Utah 84110-2550
Telephone: 801-532-1922

Date: April 4, 2005

JRD/dlm:lmh

Enclosures: Part B - Issue Fee Transmittal

Check No. 21526 in the amount of \$1,715.00

Copy of Transmittal Letter

Supplemental Application Data Sheet (4 pages)

Amendment Pursuant to 37 C.F.R. § 1.312(a) (10 pages)

Fee Addressee for Receipt of PTO Notices Relating to Maintenance Fees (2 pages)

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